



### CPU226 AC/DC/Relay

Technical specifications	
Model	CPU226 AC/DC/Relay
<b>Physical characteristic</b>	
Dimensions(W x H x D)(mm)	196x80x62
Power consume	17W
<b>Memory</b>	
Program memory size	
With run mode edit	24K
Without run mode edit	24K
Data memory	10K
<b>I/O</b>	
Local digital inputs	24 inputs
Local digital outputs	16 outputs
Digital I/O image size	256(128 inputs/128 outputs)
Analog I/O image size	64(32 inputs/32 outputs)
expansion modules allowed (Max)	7 modules
intelligent modules allowed (Max)	7 modules
Pulse catch inputs	24
High-speed counters	6 counters total
Single phase	6 at 30KHz
Two phase	4 at 20KHz
Pulse outputs	-
<b>General</b>	
Timer	Total number : 512 ( 1ms:8 ; 10ms:32 ; 100ms:472 )
Counters	512
Internal memory bits stored on power	512

down	
Time interrupts	2 with 1ms resolution
Edge interrupts	4 up and/or 4 down
Analog adjustment	2 with 8 bit resolution
Boolean execution speed	0.08µs per instruction
Real Time Clock	Built-in
<b>Communications Built-in</b>	
Ports	3 RS-485 ports, within 2 standard PPI port
PPI Baud Rates	9.6 , 19.2 and 187.5 Kbaud
Freeport Baud Rates	1.2kbaud to 115.2Kbps
Max. cable length per segment	With isolated repeater : 1000 m up to 187.5kbaud, 1200 m up to 38.4kbaud Without isolated repeater: 50 m
Max. number of stations	32 per segment, 126 per network
Max. number of masters	32
Peer to peer ( PPI Master Mode )	Yes
<b>Power</b>	
Input voltage	85 to 264 V AC(47 to 63Hz)
Input current	60/30mA while CPU only at 120/240V AC 200/100Ma while Max. load at 120/240V AC
Inrush current	20A at 264 V AC
Isolation(field to logic)	1500 V AC
Hold up time ( loss of power )	10ms at 120/240 V AC
Sensor voltage	20.4 to 28.8 V DC
Current limit	1.5A peak , thermal limit non-destructive
Ripple noise (sensor to logic)	Less than 1 V peak-to-peak
Isolation (sensor to logic)	Not isolated
<b>CPU Digital Input Specifications</b>	
Built-in digital inputs	24
Input type	PNP/NPN
Rated voltage	24 V DC at 4mA
Max. continuous permissible voltage	30V DC
Surge voltage	35 V DC,0.5s
Logic "1" voltage range	15 to 30 V DC
Logic "0" voltage range	0 to 5 V DC
Input delay	Selectable (0.2 to 12.8ms)
Connection of 2-wire proximity sensor	1mA

(Bero)	
Permissible leakage current (Max.)	
Isolation (field to logic)	Yes
Optical galvanic	500 V AC for 1minute
Isolation groups	Refer to Wiring diagram
High Speed Counter (HSC) input rate	
HSC logic level 1(15 TO 30 V DC)	20K Hz (Single phase), 10K Hz (Tow phase)
HSC logic level 1(15 TO 26 V DC)	30K Hz (Single phase), 20K Hz (Tow phase),
Inputs on simultaneously	all
Cable length Max.	
Shielded	500m normal inputs, 50m HSC inputs
unshielded	300m normal inputs
<b>CPU Digital Output Specifications</b>	
Built-in outputs	16 outputs
Output type	Dry contact
Rated voltage	24 V DC or 250 V AC
Voltage range	5 to 30 V DC or 5 to 250 V AC
Surge current (Max.)	5A for 4s at 10% duty cycle
Logic "1" (Min)	-
Logic "0" (Max)	-
Rated current per point ( Max. )	2.0A
Rated current per common ( Max )	10A
Leakage current (Max)	-
Lamp load (Max)	30W DC ; 200W AC
Inductive clamp voltage	
On State resistance (contact)	0.2Ω ( max. when new )
Isolation	
Optical (galvanic, field to logic)	-
logic to contact	1500 V AC for 1 minute
Resistance (logic to contact)	100MΩ
Isolation groups	Refer to wiring diagram
<b>Delay(Max)</b>	
Off to on	-
On to off	-
Switching	10ms
Pulse frequency (Max.)	1Hz
Lifetime mechanical cycles	10,000,000 (no load)

Lifetime contacts	100,000 (rated load)
Outputs on simultaneously	All at 60°C (horizontal)
	All at 50°C (Vertical)
Connecting two outputs in parallel	No
Cable Length (Shielded)	500m
Cable Length (unshielded)	150m
<b>Order number</b>	<b>UN 216-2BD23-0XB0</b>